

In the claims:

Following is a complete set of claims as amended with this Response.

1. (Currently Amended) A prioritized address decoder comprising:

a first comparator associated with a trusted first destination device to compare a received destination device address for ~~of~~ data with a first address range associated with the trusted first destination ~~a first device, the first comparator sending the data to the first~~ device if the destination device address is within the first address range; and

a second comparator associated with a non-trusted second destination device and coupled to the first comparator to compare the destination device address with a second address range associated with the non-trusted a second device, wherein the second comparator sends the data ~~is sent~~ to the second device only if the second comparator receives in response to a first output of the first comparator, the first output indicating that the destination device address does not correspond to the first address range and a second output of the second comparator.

2. (Original) The prioritized address decoder of claim 1, wherein the first comparator disables the second comparator when the destination device address is within the first address range.

3. (Currently Amended) The prioritized address decoder of claim 1, further comprising a third comparator coupled to the first and the second comparators to compare the destination device address with a third address range associated with a third device, wherein the data is sent to the third device only if the third comparator receives in response to a third output of the third comparator the third output indicating that the

~~destination device address does not correspond to the second address range, the second output of the second comparator, and the first output of the first comparator.~~

4. (Original) The prioritized address decoder of claim 3, wherein the third comparator is disabled when the address is within either the first address range or the second address range.

5. (Original) The prioritized address decoder of claim 1, wherein the first address range is associated with a first device of a computer system, secured data in the computer system is authorized to be sent to the first device.

6. (Original) The prioritized address decoder of claim 5, wherein the second address range is associated with a second device of the computer system, the secured data is not authorized to be sent to the second device.

7-11. (Withdrawn)

12. (Currently Amended I) A computer system comprising:

a dynamic random access memory (DRAM);

a memory controller, coupled to the DRAM, the memory controller comprising a prioritized address decoder, the prioritized address decoder including

a first comparator associated with a trusted first destination device to compare a received destination device address ~~for~~ of data with a first address range associated with the trusted first destination ~~a first device, the first comparator sending the data to the first device if the destination device address is within the first address range;~~ and

a second comparator associated with a non-trusted second destination device and coupled to the first comparator to compare the destination device address with a second address range associated with the non-trusted a second device, wherein the second

comparator sends the data is sent to the second device only if the second comparator receives in response to a first output of the first comparator, the first output indicating that the destination device address does not correspond to the first address range and a second output of the second comparator.

13. (Original) The computer system of claim 12, wherein the first comparator disables the second comparator when the destination device address is within the first address range.

14. (Currently Amended) The computer system of claim 12, wherein the prioritized address decoder further comprises:

a third comparator coupled to the first and the second comparators to compare the destination device address with a third address range associated with a third device, wherein the data is sent to the third device only if the third comparator receives in response to a third output of the third comparator the third output indicating that the destination device address does not correspond to the second address range, the second output of the second comparator, and the first output of the first comparator.

15. (Currently Amended) The computer system of claim 12, wherein the first address range is associated with destinations internal to the computer system ~~a trusted agent.~~

16. (Original) The computer system of claim 15, further comprising a processor coupled to the memory controller, wherein the trusted agent is the processor.

17. (Currently Amended) The computer system of claim 12, wherein the second address range is associated with agents coupled to an external bus ~~a non-trusted agent.~~

18. (Original) The computer system of claim 12, wherein the memory controller further comprises a plurality of configuration registers storing information on the first and the second address ranges.

19. (Original) The computer system of claim 18, wherein the information is stored in the plurality of configuration registers during configuration.

20. (Original) The computer system of claim 18, wherein the plurality of configuration registers are locked during a trusted mode.

21. (New) The prioritized address decoder of claim 1, wherein the comparators include a plurality of configuration bits corresponding to respective address ranges.

22. (New) The method of claim 21, wherein the plurality of configuration bits are software configurable.

23. (New) The method of claim 22, wherein the plurality of configuration bits are locked during a trusted mode.